

ELECTRONICS ENGINEERING SERVICES · SPAIN

Hit your milestones, without the hiring wait.

Senior testing, verification, validation and firmware teams — Spanish-led, productive in weeks and built to stay.

SEND A BRIEF →

TALK TO US

THE PROMISE



Weeks

to a productive squad



7.0%

voluntary attrition a year



Spanish-led

engineering accountability



Reviewed

before a squad reaches you

Faster than hiring. Built to stay.

Capacity arrives on your timescale — and stays long enough for knowledge to compound.

PRODUCTIVE SQUAD

Weeks

Capacity on the programme’s timescale.

LIVE ENGAGEMENTS

VOLUNTARY ATTRITION · YEAR

7.0%

The core team that starts is the core team that finishes.

INTERNALLY AUDITED

MULTI-YEAR RETENTION

~74%

All causes, including performance-managed exits.

METHODOLOGY PENDING

LIVE ROSTER

Now

Vetted engineers, mapped and ready.

IMMEDIATE AVAILABILITY

Engineering capacity for programmes that cannot wait.

From a global semiconductor leader to a sub-fifty-person startup — the qualifying question is whether the work matches our disciplines.

 Electronics & semiconductor01	 Mid-sized & smaller firms02	 Funded technology startups03
ESTABLISHED Multi-year silicon, firmware or embedded programmes with scarce verification and test seats they cannot fill at pace.	SPECIALIST A precise technical match in mixed-signal, RF, ultra-low-power, imaging or industrial electronics.	SCALING Teams that must build engineering capacity faster than any realistic hiring plan allows.

You qualify on technical fit, not size.

Nothing here is enterprise-only.

The scarce disciplines your programme cannot wait for.

Five clear capability groups, with independent testing and verification at the centre.

✓ Testing & Certification01

✓ Functional

✓ System

✓ Compliance

✓ Bluetooth

✓ Verification & Validation02

✓ RTL

✓ Regression

✓ Coverage

✓ Requirements

↺ DevOps03

✓ CI/CD

✓ Build & release

✓ QEMU / FPGA

✓ Security & Reliability04

✓ Root of trust

✓ Robustness

✓ Performance

⌘ Customer Engineering05

✓ BSP

✓ OEM release

✓ Platform support

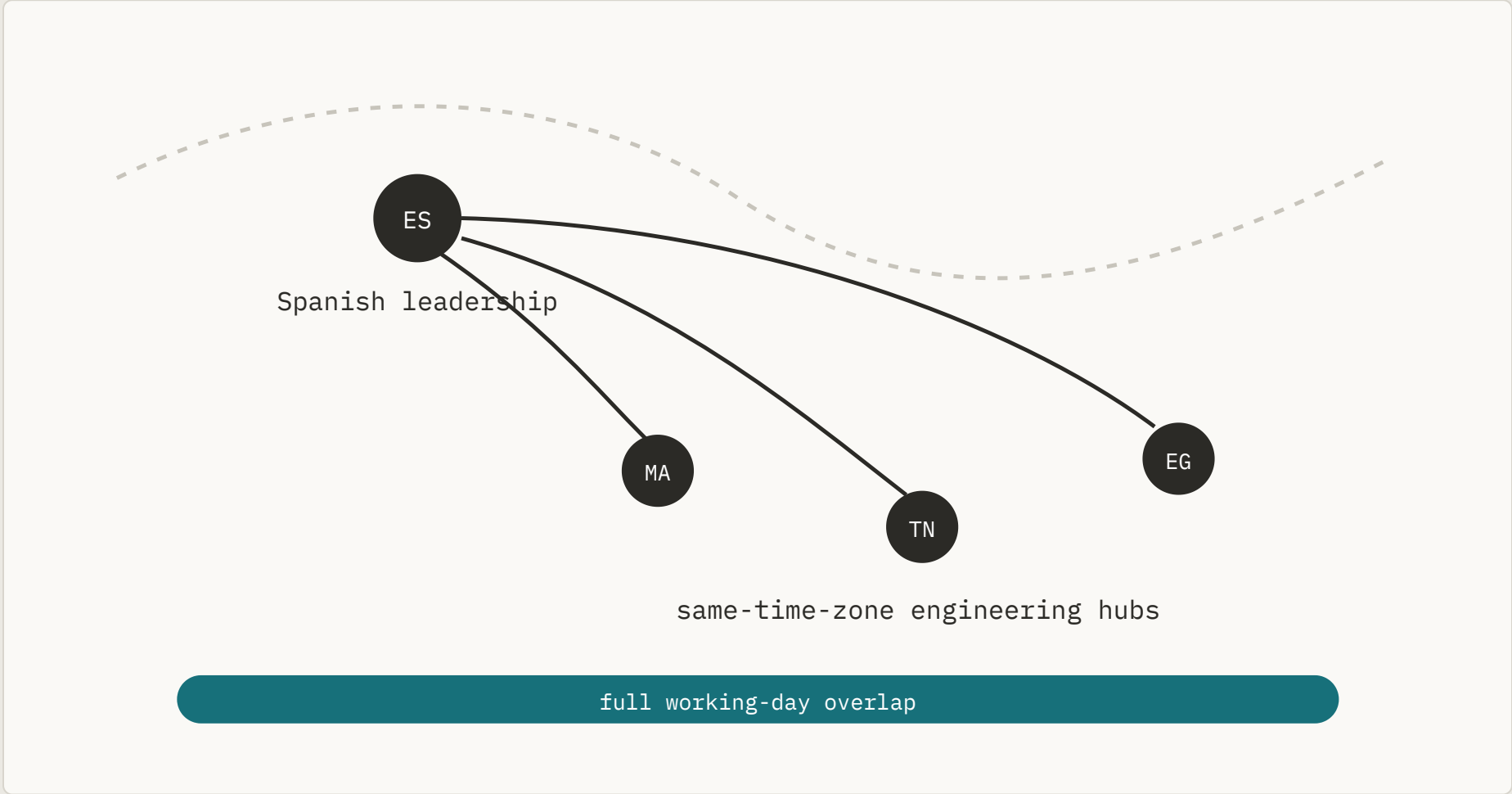
RTL verification · Bluetooth certification · CI/CD for firmware · BSP integration · SerDes analog verification · DFT · mixed-signal simulation · QEMU/FPGA automation · SPI/I²C/UART/USB drivers

You choose the control, capacity and budget.

MODEL	CONTROL	CAPACITY	BEST FIT
 Spanish Team	Highest proximity	Focused	Quality-first entry
 Hybrid Team	Spanish-led	Expanded	Scale + same time zone
 Distributed Team	Spanish-led	Broadest reach	Value + capacity
 Specialist Engineers	Named expert	Precise	Urgent skill gap

Spanish-led accountability holds the team models together; specialist engineers join as named independent experts.

More team. Same working day.



SAME WORKING HOURS

We're online when you are.



IN-PERSON WORKSHOPS

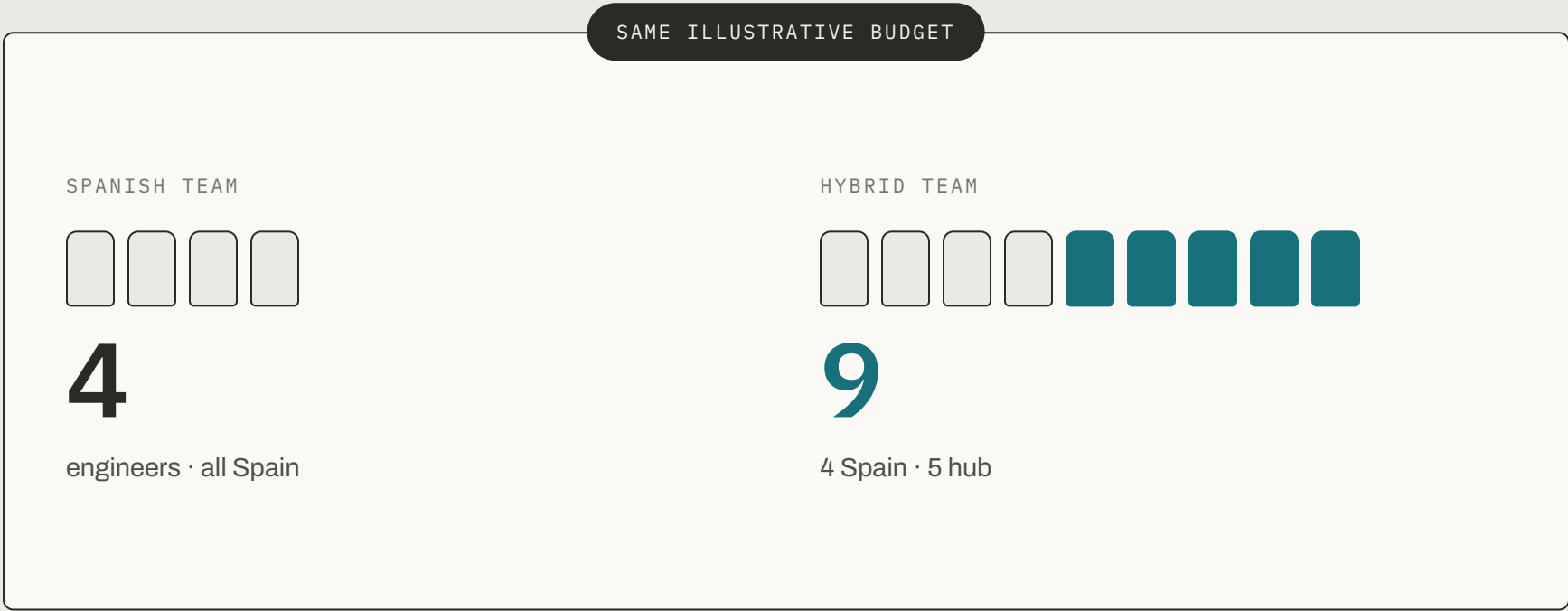
We fly in when it counts.



SCALE FOR THE BUDGET

A bigger team, same budget.

Same budget. Bigger team.



Scale capacity against the verification backlog while retaining Spanish-led accountability.

The saving is real.
More capacity, same budget.

A materially larger senior team for the same spend — Spanish-led from first commit.

Accountability, not anonymous capacity.

A delivery model built around named leadership,
independent assurance and continuity.

01 **Spanish engineering leadership**
Named technical review and accountability on every team engagement.

02 **Independent testing & verification**
Structurally separate from the team whose work it checks.

03 **Engineers built to remain**
Context accumulates across programme phases instead of rotating out.



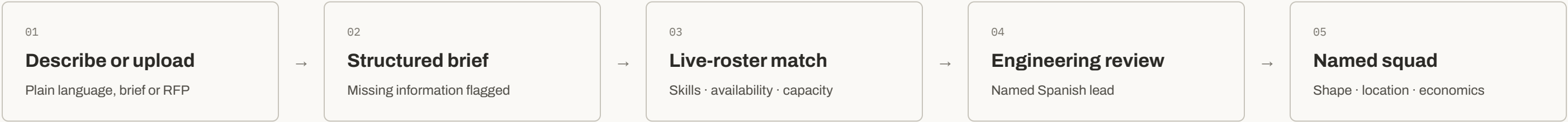
REVIEWED & SIGNED

**The Hallmark is applied only
after named engineering review.**

Teal marks an attested state — never generic decoration.

Describe your project. Meet your squad.

Digital up to a credible proposal. Human-led from there.



Human review is the control point.

AI supports the process; it does not replace technical judgement.

SEND A BRIEF →

Proof you can take to the board.

Anonymised here until partner sign-off. Named references are available under NDA.

GLOBAL SEMICONDUCTOR LEADER

BSP integration & OEM release support

Platform integration alongside the partner’s own teams.

SECURITY-IP COMPANY

Security-software testing & release verification

Independent assurance for security-critical products.

SWISS ULTRA-LOW-POWER CHIPMAKER

QA, firmware verification & wireless certification

Software QA, firmware and Bluetooth certification.

FUNDED SATELLITE-CONNECTIVITY STARTUP

Algorithm validation & RTL verification

MATLAB/C++, RTL and mixed-signal SoC work.

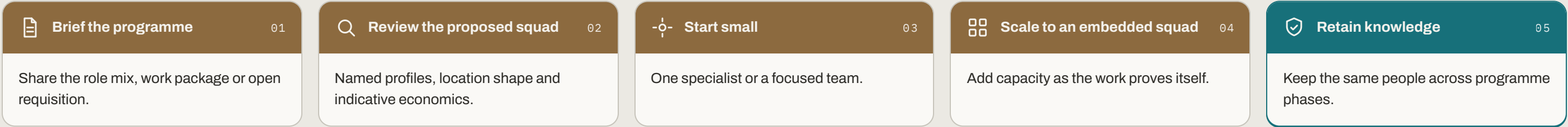
7.0% INTERNALLY AUDITED

~74% METHODOLOGY PENDING

REFERENCES UNDER NDA

Start with one specialist. Scale when it works.

One specialist can solve an urgent gap and prove the model before you commit wider.



Your squad in days, not months.

Every brief stays private and every proposed squad is reviewed by named engineering leadership.

BUILD A BRIEF →

SEE TT AI

TALK TO US

01

Upload what you have

TT AI checks the brief and flags anything missing.

02

Build it with TT AI

Start directly in the chat box on the Tech Teamz home page.

03

Talk to Spanish engineering leadership

Skip the platform and scope the work directly.



Reviewed & signed before it reaches you